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INFORMATION DISCLOSURE STATEMENT

Applicant(s)

Murthi Nanja

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**U.S. PATENT DOCUMENTS**

| Examiner Initials | Document Number | Issue or Publication Date | Name | Class | Subclass | Filing Date (If Appropriate) |
|----------------------|--------------------|------------------------------|------|-------|----------|---------------------------------|
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FOREIGN PATENT DOCUMENTS

| Examiner Initials | Document Number | Publication Date | Country | Translation | |
|----------------------|--------------------|---------------------|---------|-------------|----|
| | | | | Yes | No |
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OTHER DOCUMENTS

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| EC | Pering et al., "Dynamic Voltage Scaling and the Design of a Low-Power Microprocessor System", University of California Berkeley, Electronics Research Laboratory, 6 pages (1998) |
| EC | Govil et al., "Comparing Algorithms for Dynamic Speed-Setting of a Low-Power CPU", Computer Science Division University of California, pp 13-26 (1995) |
| EC | Grunwald et al., "Policies for Dynamic Clock Scheduling", Department of Computer Science University of Colorado, 14 pages (2000) |

EXAMINER: /Eric Chang/

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